

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tosuke KAWADA

Application No.: Rule 1.53(b) Divisional Application of  
Application No. 09/524,565 filed  
March 13, 2000

Filed: August 27, 2003

Docket No.: 105687.01

For: METHOD FOR MOUNTING ELECTRIC COMPONENTS ON A PRINTED-WIRING  
BOARD (AS AMENDED)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The references were cited by or submitted to the Office in parent application No. 09/524,565, filed March 13, 2000, which is relied upon for an earlier filing date under 35 U.S.C. §120. Thus, copies of these references are not attached. 37 CFR §1.98(d).
- ☒ 3. U.S. Patent No. 6,161, 277 is a resulting patent from U.S. Patent Application No. 08/977,662 cited in the Parent Application 09/524,565.

- ☒ 4. U.S. Patent No. 5,960,534 is of the family of EP 0 691 804 A1.

Respectfully submitted,



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| Form PTO-1449<br>(REV. 8-83)  |  | US Dept. of Commerce<br>PATENT & TRADEMARK OFFICE                                |         | ATTY DOCKET NO.<br>105687.01                                     |       | APPLICATION NO.<br>Rule 1.53(b) Divisional<br>Application of Application<br>No. 09/524,565 |  |
| INFORMATION DISCLOSURE STATEMENT<br><br>(Use several sheets if necessary)   |  |  |         | APPLICANT<br>Tosuke KAWADA<br><br>FILING DATE<br>August 27, 2003 |       |  |  |
|   |  |  |         |  |       |  |  |
| U.S. PATENT DOCUMENTS   |  |  |         |  |       |  |  |
| EXAMINER<br>INITIAL   |  | DOCUMENT NUMBER  | DATE    | NAME   | CLASS | SUB<br>CLASS   |  |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)  |  |  |         |  |       |  |  |
|   |  | IBM Technical Disclosure Bulletin, Vol. 30, No. 10, pp. 47-49, (Armonk, NY, USA) |         |  |       |  |  |
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| EXAMINER  |  |  |         | DATE CONSIDERED  |       |  |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |  |  |         |  |       |  |  |

Date: August 27, 2003